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Docket No. 0756-1630

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of)
Shunpei YAMAZAKI et al.)
Serial No. 08/799,506) Art Unit: 2822
Filed: February 12, 1997) Examiner: M. Wilczewski
For: METHOD FOR PROCESSING SEMICONDUCTOR)
DEVICE APPARATUS FOR PROCESSING A)
SEMICONDUCTOR AND APPARATUS FOR)
PROCESSING SEMICONDUCTOR DEVICE)

TERMINAL DISCLAIMER

Honorable Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

I, Dr. Shunpei Yamazaki, having a place of business at Semiconductor Energy Laboratory Co., Ltd., 398, Hase, Atsugi-shi, Kanagawa-ken 243-0036 Japan, state that I am authorized to sign on behalf of the assignee of this invention and that the Assignment referred to below has been reviewed and certify that, to the best of my knowledge and belief, the entire right, title and interest in the above-identified application is in the name of Semiconductor Energy Laboratory Co., Ltd. by virtue of an Assignment recorded in the U.S. Patent and Trademark Office at Reel 7238, Frame 0716.

Semiconductor Energy Laboratory Co., Ltd. hereby disclaims, except as provided below, the terminal part of the statutory term of any patent granted on the instant application, which would extend beyond the expiration date of the full statutory term defined in 35 U.S.C. 154 to 156 and 173, as presently shortened by any terminal disclaimer, of prior Patent No. 6,329,229.

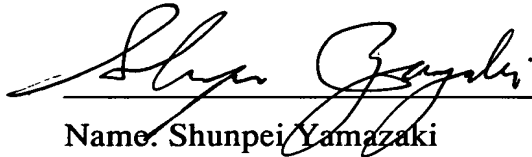
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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

02/05/2004

Date



Name: Shunpei Yamazaki

Title: President

Company Name: Semiconductor Energy
Laboratory Co., Ltd.